



#16/c
3-12-03
Maurice

PATENT APPLICATION

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of

Docket No: Q60884

Masayuki MIZUNO

Appln. No.: 09/664,094

Group Art Unit: 2826

Confirmation No.: 5281

Examiner: Johannes P MONDT

Filed: September 19, 2000

For: SEMICONDUCTOR INTEGRATED CIRCUIT

AMENDMENT UNDER 37 C.F.R. § 1.111

Commissioner for Patents
Washington, D.C. 20231

Sir:

In response to the Office Action dated December 4, 2002, please amend the above-identified application as follows:

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IN THE CLAIMS:

Please enter the following amended claims:

-
1. (Thrice Amended) A semiconductor integrated circuit comprising a microstrip structure comprising:
- C¹ a signal line; and
- a ground plate,
- wherein at least one through hole is formed in said signal line, and an inner wall of said through hole is only directly electrically connected to said signal line.